Review of: "Pulse Amplitude Measurement Using Low Sampling ADC and Interpolation Technique"

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The authors proposed pulse amplitude measurement techniques with four different types of interpolation based on the implementation of FPGA. The paper reviewed each interpolation theory thoroughly. The authors presented an FPGA implementation with a 12-bit ADC to check the accuracy of the interpolation techniques. The accuracy comparison of the four interpolation techniques is presented with detailed error calculation, which is the strength of the paper. Only one comment on this paper is that there are several high-resolution ADCs in the market, such as 14-bit, 16-bit, and etc.; one more FPGA implementation with higher-resolution ADCs (14-bit, 16-bit, …) would generalize the proposed interpolation measurement techniques. In general, this paper presented excellent research work to help others make pulse amplitude measurements with high-resolution ADCs.