

Peer Review

Review of: "WebRISC-V: A 64-bit RISC-V Pipeline Simulator for Computer Architecture Classes"

Giulio Malenza¹

1. University of Turin, Italy

WebRISC-V application is useful for simulating the pipelined execution of assembly RV64IM programs. It can be useful for students who are approaching the subject for the first time. It is certainly also useful for professors who need to explain the topic.

- Can you provide specific examples of how WebRISC-V compares to other tools in terms of features or user experience?

WebRISC-V can be used during live demonstrations where an instructor projects the tool and walks through program execution in class. For example, during a lecture on pipelining or function calls, the instructor could write a short program and use WebRISC-V to illustrate how the stack behaves or how function return addresses are managed. This real-time, visual representation helps bridge the gap between lecture material and practical implementation.

- What are some potential improvements or additional features that could enhance the educational value of WebRISC-V?

Compared to other similar tools, WebRISC-V emphasizes the core instruction set, helping students develop a strong foundational understanding of RISC-V. It also aligns well with modern computer architecture curricula, which are increasingly adopting RISC-V as the standard ISA for instruction.

Declarations

Potential competing interests: No potential competing interests to declare.