Review of: "Pulse Amplitude Measurement Using Low Sampling ADC and Interpolation Technique"

Zbigniew Hajduk[1]

1 Rzeszów University of Technology

The comparison results of 7 interpolation techniques obtained by the authors are interesting and might be a valuable tip for designers. However, it is a pity that the implementation of a selected method of interpolation has not been presented in more detail. But this may be the subject of a future article. The use of the LabView environment, of course, greatly facilitates the implementation of specific algorithms, but in real cases, it is often necessary to implement these algorithms in an FPGA using classic coding in the hardware description language or using high-level synthesis techniques. Comparing, for example, the implementation complexity of the presented techniques from the point of view of digital hardware design would be additional valuable knowledge.