

Peer Review

Review of: "A Review of Formal Methods in Quantum Circuit Verification"

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The main problem I have with this work is that it is only a list of possible solutions one could use. There is nothing concrete, nor is there a sound and principled discussion of the limitations of the various approaches. Some case studies and comparisons would help better frame the work. Some concrete examples and pieces of real specifications are needed to let the reader understand what is going on.

Declarations

Potential competing interests: No potential competing interests to declare.