

Review of: "Pulse Amplitude Measurement Using Low Sampling ADC and Interpolation Technique"

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Potential competing interests: No potential competing interests to declare.

The article compares several signal interpolation methods implemented on an FPGA. The paper is well written and easy to understand.

However, the test signal used is a smooth Gaussian curve, made of around 8k samples. The signal ranges over an 8ms timeframe and is sampled at a 1kHz rate. So there is no real challenge in this test case. It would be more interesting to compare these interpolation methods on more complex signals, i.e., non-continuous signals or higher bandwidth signals.

A summary table would be appreciated to compare the performances of all the methods, with their computing burden as well as the various errors.