

# Review of: "Pulse Amplitude Measurement Using Low Sampling ADC and Interpolation Technique"

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**Potential competing interests:** No potential competing interests to declare.

The article compares various signal interpolation methods implemented on an FPGA. The article is generally well-written, descriptive, and understandable.

However, the figures given are not clear, and their explanations are not sufficient.

Users request a general table showing the performance, efficiency, and computational load of all methods.