

Review of: "Pulse Amplitude Measurement Using Low Sampling ADC and Interpolation Technique"

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Potential competing interests: No potential competing interests to declare.

In this paper, various interpolation techniques are discussed and their performance is measured in terms of RE and MSE. Simply for the measurement of RE and MSE, FPGA is not necessary. It will be good enough if the device utilization summaries of the FPGA devices for all the techniques that are taken into study are included.