

Review of: "Pulse Amplitude Measurement Using Low Sampling ADC and Interpolation Technique"

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Potential competing interests: No potential competing interests to declare.

Areas for Improvement:

Theoretical Justification: While the article presents a variety of interpolation methods, a more thorough theoretical discussion on why these specific methods were chosen and their expected performance in this context would strengthen the paper.

Comparative Analysis: The paper would benefit from a more nuanced comparison of the interpolation methods, perhaps discussing the trade-offs between complexity, accuracy, and hardware resource utilization in more detail.

Broader Context: While the focus on FPGA implementation is clear, the paper could discuss how these findings relate to broader trends or challenges in ADC technology, potentially offering insights into future research directions or applications.

Statistical Analysis: Incorporating more statistical analysis to assess the significance of the observed differences in performance between methods could provide a stronger foundation for the conclusions drawn.

User Guidance: Offering more explicit guidance for practitioners on choosing interpolation methods based on specific application requirements would enhance the article's practical value.