Review of: "Pulse Amplitude Measurement Using Low Sampling ADC and Interpolation Technique"

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Potential competing interests: No potential competing interests to declare.

Overall, this paper presents a valuable contribution to the field of signal processing and measurement techniques. The authors have provided a comprehensive comparison of various interpolation methods for measuring analog pulse amplitudes using low sampling rate ADCs. The paper is technically sound, and the subject matter is presented in a clear and comprehensive manner.

The experimental setup and validation process are well-described, and the authors have provided a thorough analysis of the performance of each interpolation method based on relevant metrics such as relative error, mean square error, and mean absolute deviation. Additionally, the hardware resource utilization on the FPGA for each method is also evaluated, which is a valuable consideration for practical implementations.

While the references provided are relevant, the authors could consider including a few additional references related to the specific application areas mentioned to provide more context. Additionally, there are a few minor grammatical and expression issues that could be addressed to improve the overall clarity and readability of the paper.

In summary, this paper is a valuable contribution to the existing body of knowledge and is recommended for publication after addressing the minor issues mentioned above.

In addition, the English language used in the paper is good. However, there are a few minor grammatical and expression issues that could be improved. For example:

- In the abstract, "...were conducted for measurement of analog pulse peak height." could be rephrased as "...were conducted to measure the analog pulse peak height."
- In Section 2.1, "The greatest integer value that is less than or equal to x in this case is called the floor function [x]." could be rephrased as "The greatest integer value less than or equal to x is called the floor function [x]."
- In Section 3.2, "Each interpolation technique discussed in earlier section is implemented in FPGA separately." could be rephrased as "Each interpolation technique discussed in the earlier section is implemented separately on the FPGA."